

AUTOMOTIVE CURRENT TRANSDUCER HC2F80-N CLIPS









Introduction

The HC2F CLIPS Family is for the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC2F CLIPS family gives you the choice of having different current measuring ranges in the same housing.

Features

- Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 40 A up to 200 A
- Maximum RMS primary admissible current: defined by busbar to have T° < + 150°C
- Operating temperature range: 40°C < T° < + 125°C
- Output voltage: full ratio-metric (in sensitivity and offset)
- · Compact design for PCB mounting.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- · Very low thermal sensitivity drift
- Wide frequency bandwidth
- · No insertion losses.

Automotive applications

- Electrical Power Steering
- Starter Generators
- Converters ...

Principle of HC2F CLIPS Family

The open loop transducers uses a Hall effect integrated circuit. The magnetic flux density B, contributing to the rise of the Hall voltage, is generated by the primary current I_p to be measured. The current to be measured I_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, B is proportional to:

$$\mathbf{B} (\mathbf{I}_{p}) = \text{constant (a) } \mathbf{X} \mathbf{I}_{p}$$

The Hall voltage is thus expressed by:

$$V_{\perp} = (R_{\perp}/d) \times I \times constant (a) \times I_{p}$$

Except for $\mathbf{I}_{\mathrm{p}^{\mathrm{s}}}$ all terms of this equation are constant. Therefore:

$$V_{H}$$
 = constant (b) x I_{P}

The measurement signal $\mathbf{V}_{\rm H}$ amplified to supply the user output voltage or current.

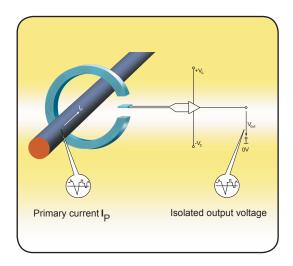


Fig. 1: Principle of the open loop transducer



15.24 (x2)

ΡЗ

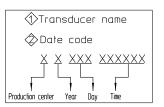
0.64 ±0.05 (×4)

Dimensions HC2F xxx-N CLIPS family (in mm.)

Secondary connection

Terminals	Designations
3	Supply voltage + 5 V DC
1	V _{OUT}
2	Ground
E1 to E4	Ground (*)

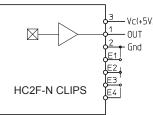
P1

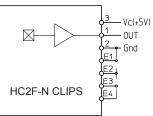


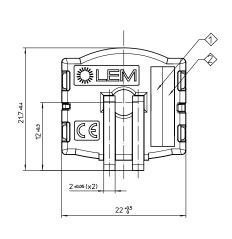
Positive primary current direction

(*) Only 1 of these 4 pins could be connected

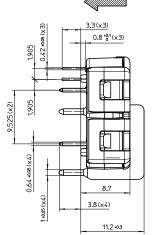
Connection







0.36 ±0.05 (x3)



Bill of materials

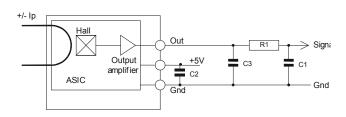
PBT GF 30 Plastic case Magnetic core FeSi alloy Pins and primary bus bar Copper alloy tin plated (lead free)

Remarks

• General tolerance ± 0.2 mm

• $V_{OUT} > \frac{V_c}{2}$ when I_p flows in the direction of the arrow.

Electronic schematic



9 g

Power supply decoupling capacitor: C2 = 47 nF

EMC protection capacitor C3 = 4.7 nF

Optional:

High frequency signal noise filter:

 $R1 > 100 \Omega$

C1 = defined according to the system frequency bandwidth

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Mass



Primary bus bars connected in parellel

$$IN \xrightarrow{\begin{array}{c} P1 \\ \hline \\ P3 \end{array}} \bigcirc \cdots \bigcirc \stackrel{P2}{ \hline \\ P4 \end{array} \longrightarrow OUT$$

Absolute maximum ratings (not operating)

Parameter	Symbol		Specification	Conditions
Maximun peak primary current (not operating)	I _{P max}	А	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I _{PN}	Α	Defined by busbar to have T° ≤ 150°C	
Maximun supply voltage (not operating)	V _{C max}	V	7	
Secondary maximum admissible power	P _{S max}	mW	150	
Ambient operating temperature	T _A	°C	- 40 < T _A < 125°C	
Ambient storage temperature	T _s	°C	- 40 < T _s < 125°C	
Electrostatic discharge voltage	V _{ESD}	V	2000	see page 5/5
Maximum admissible vibration	γ	m.s -2	100	see page 5/5
Rms voltage for AC isolation test 50 Hz, 1 min	V _d	V	1500	
Clearance distance	dCI	mm	1.67	
Creepage distance	dCp	mm	1.8	CTI = 425

Operating characteristics

Danier aten	Symbol	Unit	5	pecificati	on	0 4141	
Parameter			Min	Typical	Max	Conditions	
Primary current, measuring range	I _{PM}	Α	-80		80	@ - 40°C < T° < 125°C	
Supply voltage 1)	V _c	V	4.75	5.00	5.25	@ - 40°C < T° < 125°C	
Output voltage (Analog)	V _{OUT}	V	V _{OUT} = (V	_c /5) x (2.5 +	0.025 x I _P)	@ - 40°C < T° < 125°C	
Sensitivity	G	V/A	0.0245	0.025	0.0255	0 T _A = 25°C; V _C = 5.00 V	
Offset voltage	V _o	V	2.46	2.5	2.54	@ V _C = 5.00 V; T _A = 25°C; I _P = 0 A	
Current consumption	I _c	mA	-	13.5	20	@ - 40°C < T° < 125°C; 4.75 V < V _C < 5.25 V	
Load resistance	R _L	ΚΩ	2	-	-		
Output internal resistance	R _{OUT}	Ω	-	-	10		
	Performance Data						
Sensitivity error	$\epsilon_{_{ m G}}$	%	-2.0	± 0.7	2.0	0 T _A = 25°C, V _C = 5.00 V; Gth = 0.025 V/A	
Electrical offset	I _{OE}	Α	-0.56	± 0.25	0.56	@ V _C = 5.00 V; T _A = 25°C	
Electrical offset	V _{OE}	mV	-14	± 6	14		
Magnetic offeet	I _{OM}	Α	-1.00	± 0.7	1	After every reion to 11.T = 25°C	
Magnetic offset	V _{OM}	mV	-25	± 17.5	25	@ After excursion to $\pm I_p$; $T_A = 25^{\circ}C$	
I _{OE}	TCI _{OEAV}	mA/°C	-6.0	± 2.8	6.0		
Average temperature coefficient of						@ - 40°C < T° < 125°C; V _C = 5.00 V	
V_{OE}	TCV _{OEAV}	mV/°C	-0.15	± 0.07	0.15		
Average temperature coefficient of G	TCG _{AV}	%/°C	-0.04	± 0.02	0.04	@ - 40°C < T° < 125°C; V _C = 5.00 V	
Linearity error	ε	% I _P	-1.0	± 0.3	1.0	@ I _p ; V _C = 5.00 V, T _A = 25°C	
Response time	t,	μs	-	15	20	@ di/dt = 50 A/µs; I _T = 60 A	
Frequency bandwidth 2)	BW	kHz	20	-	-	@ -3 dB; I _T = 20 A rms	
Output voltage noise peak-peak	V _{no p-p}	mV	-	37	50	@ T _A = 25°C; 0 Hz < f < 1 MHz	
Output voltage noise rms	V _{no rms}	mV	-	4	5	@ T _A = 25°C; 0 Hz < f < 1 MHz	

Notes: 1) The output voltage \mathbf{V}_{OUT} is fully ratiometric. The offset and sensitivity are dependent on the supply voltage \mathbf{V}_{C} relative to the following formula:

$$I_{P} = \left(V_{\text{OUT}} - \frac{V_{\text{c}}}{2}\right) \times \frac{1}{G} \times \frac{5}{V_{\text{c}}} \quad \text{with G in (V/A)}$$

²⁾ Small signal only to avoid excessives heatings of the busbar, the magnetic core and the ASIC.



Primary bus bars connected in series

Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Conditions
Maximun peak primary current (not operating)	I _{P max}	А	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I _{PN}	Α	Defined by busbar to have T° ≤ 150°C	
Maximun supply voltage (not operating)	V _{C max}	V	7	
Secondary maximum admissible power	P _{S max}	mW	150	
Ambient operating temperature	T _A	°C	- 40 < T _A < 125°C	
Ambient storage temperature	T _s	°C	- 40 < T _s < 125°C	
Electrostatic discharge voltage	V _{ESD}	V	2000	see page 5/5
Maximum admissible vibration	γ	m.s -2	100	see page 5/5
Rms voltage for AC isolation test 50 Hz, 1 min	V _d	V	1500	
Clearance distance	dCI	mm	1.67	
Creepage distance	dCp	mm	1.8	CTI = 425

Operating characteristics

Parameter	Symbol	Unit	5	Specification	on	O an distinguish	
			Min	Typical	Max	Conditions	
			Electrica	I Data			
Primary current, measuring range	I _{PM}	Α	-40		40	@ - 40°C < T° < 125°C	
Supply voltage 1)	V _c	V	4.75	5.00	5.25	@ - 40°C < T° < 125°C	
Output voltage (Analog)	V _{OUT}	V	V _{OUT} = (\	/ _c /5) x (2.5 +	-0.05 x I _P)	@ - 40°C < T° < 125°C	
Sensitivity	G	V/A	0.049	0.05	0.051	@ T _A = 25°C; V _C < 5.00 V	
Offset voltage	V _o	V	2.46	2.5	2.54	@ V _C = 5 V; T _A = 25°C; I _P = 0 A	
Current consumption	I _c	mA		13.5	20	@ - 40°C < T° < 125°C; 4.75 V < V _C < 5.25 V	
Load resistance	R _L	ΚΩ	2	-	-		
Output internal resistance	R _{OUT}	Ω	-	-	10		
		P	erforman	ce Data			
Sensitivity error	$\epsilon_{_{ m G}}$	%	-2.0	± 0.7	2.0	@ $T_A = 25^{\circ}C$, $V_C = 5.00 \text{ V}$; Gth = 0.05 V/A	
Electrical offset	I _{OE}	Α	-0.28	± 0.12	0.5		
Electrical offset	V _{OE}	mV	-14	± 6	14		
Magnetic offeet	I _{OM}	Α	-0.5	± 0.35	0.12	@ After every reion to 11: T = 25°C	
Magnetic offset	V _{OM}	mV	-25	± 17.5	25	@ After excursion to $\pm I_p$; $T_A = 25^{\circ}C$	
I _{OE}	TCI _{OEAV}	mA/°C	-3.0	± 1.4	3.0		
Average temperature coefficient of						@ - 40°C < T° < 125°C; V _C = 5.00 V	
V_{OE}	TCV _{OEAV}	mV/°C	-0.15	± 0.07	0.15		
Average temperature coefficient of G	TCG _{AV}	%/°C	-0.04	± 0.02	0.04	@ - 40° C < T° < 125° C; V_{c} = 5.00 V	
Linearity error	$\epsilon_{\scriptscriptstyle \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \!$	% I _P	-1.0	± 0.3	1.0	$@I_{P}; V_{C} = 5.00 \text{ V}, T_{A} = 25^{\circ}\text{C}$	
Response time	t,	μs	-	15	20	@ di/dt = 50 A/µs; I _T = 30 A	
Frequency bandwidth 2)	BW	kHz	20	-	-	@ -3 dB; I _T = 10 A rms	
Output voltage noise peak-peak	V _{no p-p}	mV	-	37	50	@ T _A = 25°C; 0 Hz < f < 1 MHz	
Output voltage noise rms	V _{no rms}	mV	-	4	5	@ T _A = 25°C; 0 Hz < f < 1 MHz	

Notes: 1) The output voltage \mathbf{V}_{OUT} is fully ratiometric. The offset and sensitivity are dependent on the supply voltage \mathbf{V}_{C} relative to the following formula:

$$I_{P} = \left(V_{OUT} - \frac{V_{c}}{2}\right) \times \frac{1}{G} \times \frac{5}{V_{c}} \quad \text{with G in (V/A)}$$

²⁾ Small signal only to avoid excessives heatings of the busbar, the magnetic core and the ASIC.



PERFORMANCES PARAMETERS DEFINITIONS

Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear $\mathbf{I}_{\rm C}$ amplifier gain.

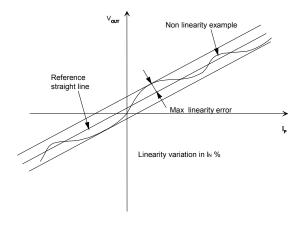
Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $I_{P\,max}$.

Linearity:

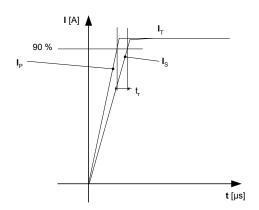
The maximum positive or negative discrepancy with a reference straight line $V_{\text{OUT}} = f(I_p)$.

Unit: linearity (%) expressed with full scale of I_{P may}.



Response time (delay time) t.:

The time between the primary current signal and the output signal reach at 90 % of its final value



Typical:

Theorical value or usual accuracy recorded during the production.

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $V_{out} = f(I_p)$, it must establish the relation:

$$V_{out}(I_p) = V_c/5 (G \times I_p + 2.5) (*)$$

(*) For all symetrics transducers.

Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25°C.

The offset variation I_{OT} is a maximum variation the offset in the temperature range:

$$I_{OT} = I_{OE} \max - I_{OE} \min$$

The Offset drift \mathbf{TCI}_{OEAV} is the \mathbf{I}_{OT} value divided by the temperature range.

Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25°C.

The sensitivity variation \mathbf{G}_{T} is the maximum variation (in ppm or %) of the sensitivity in the temperature range:

 $\mathbf{G}_{\scriptscriptstyle T}$ = (Sensitivity max - Sensitivity min) / Sensitivity at 25°C.

The sensitivity drift \mathbf{TCG}_{AV} is the \mathbf{G}_{T} value divided by the temperature range.

Offset voltage @ $I_p = 0$ A:

Is the output voltage when the primary current is null. The ideal value of $\mathbf{V}_{\rm O}$ is $\mathbf{V}_{\rm C}/2$ at $\mathbf{V}_{\rm C}$ = 5 V. So, the difference of $\mathbf{V}_{\rm O}$ - $\mathbf{V}_{\rm C}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Environmental test specifications

Name	Standard	Conditions			
Low T° storage	IEC 60068 Part 2-1	T° - 40°C / 100 H not connected			
Thermal shocks	IEC 60068 Part 2-14	T° - 30°C to 110°C /1000 cycles not connected			
Low T°operation at min supply voltage	IEC 60068 Part 2-1	T° - 40°C / 1000 H supply voltage = 4.75 V			
Hight T° operation at max supply voltage	IEC 60068 Part 2-2	T° 125°C / 1000 H supply voltage = 5.25 V			
Temperature humidity bias	IEC 60068 Part 2-3	T° 90°C / 95 % RH/ 1000 H supply voltage = 5.25 V			
Pressure cooker		T° 125°C / 100 % RH, P 0.178 Mpa/100 H supply voltage = 5 V			
Mechanical Tests					
Vibration	IEC 60068-2-64	Room T°, acceleration 100 m/s2, frequency 20 to 500 Hz/96 H each axis			
Drop test	IEC 60068 Part 2-29	Heigh 750 mm concret floor each directions			